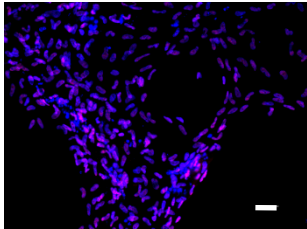
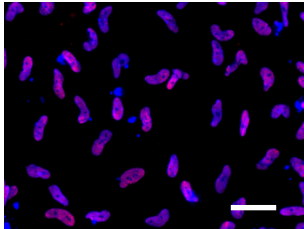


SYN (d32)

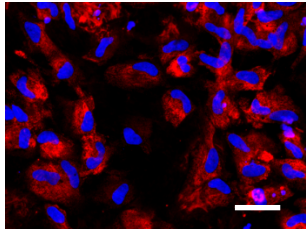
SCX



MKX



COL1A1



COL1A2

