Description of 'CAD_Geometry_File.gds': The supplemental CAD file is a Graphic Database System (GDSII) file which contains 9 separate layers. The layer 'full wafer design' is the final layer which represents the top view of the final mask design. Layer 'Channel' refers to a rectangle which is used to design rectangular portions of the channel. Layer 'external rectangle array' contains the design of the large rectangular strips which delineate the outer border in the layer 'full wafer design'. Two separate layers which are labeled as 'name_1' and 'name_2' contain the name of institute and the author, respectively. The layer 'circles_around' consists of a circle which is used for designing the circular shapes around the channel. One layer, 'single_circle_pillar' layer contains the design of a single circle. The other layer is array of this single circle named as 'pillar'. Finally, 'single chip' layer demonstrates one single microchip design.